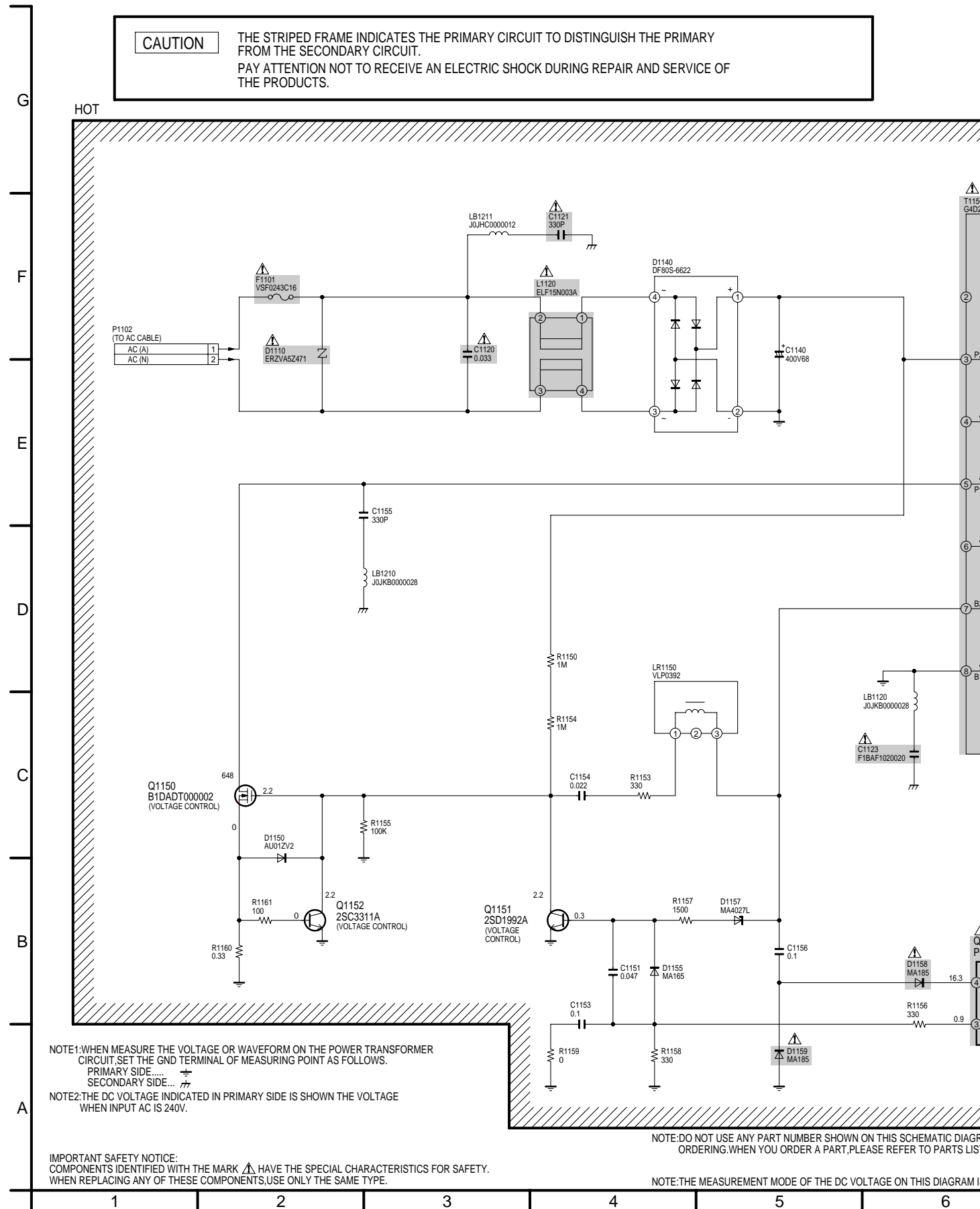
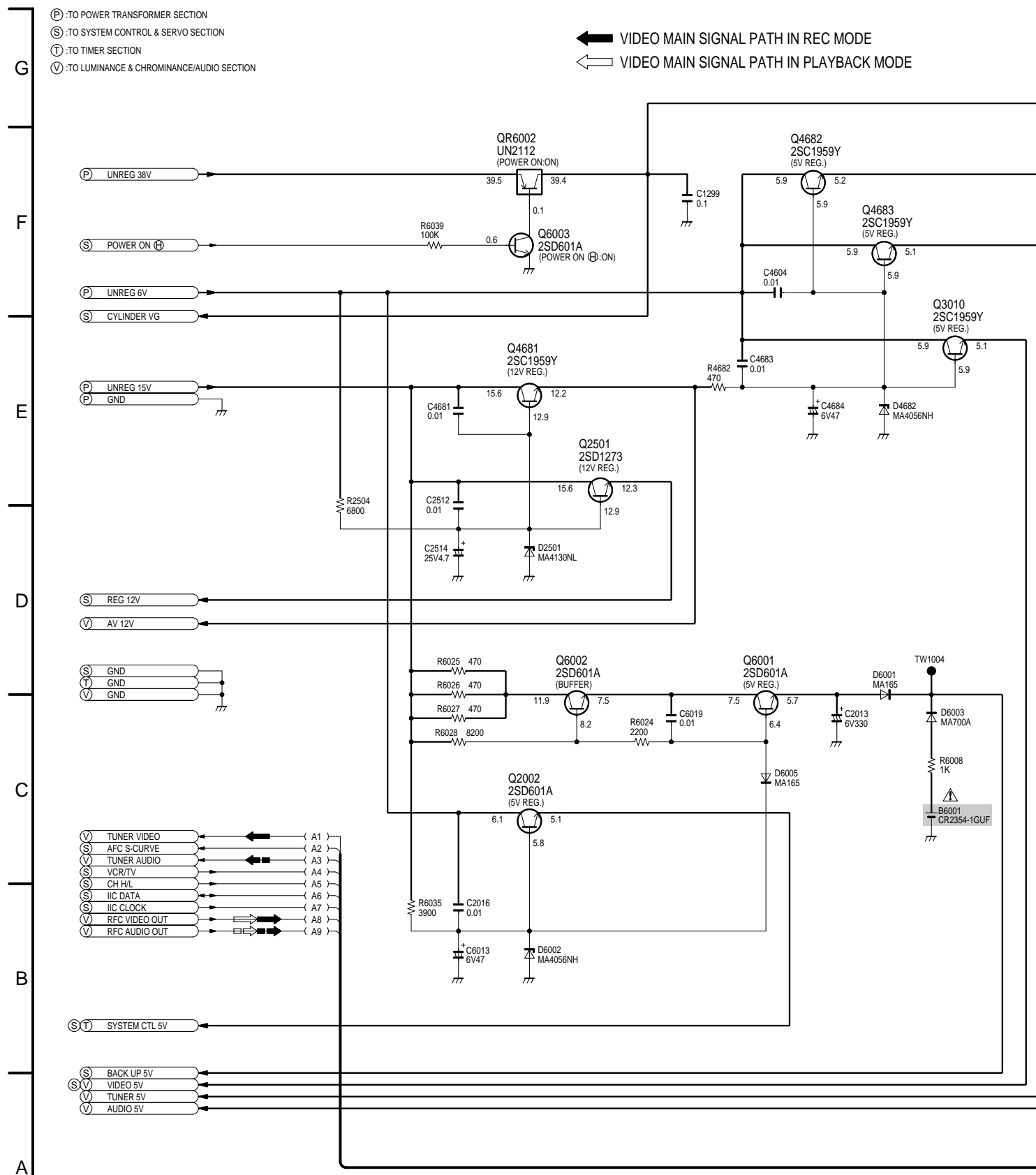


8 SCHEMATIC DIAGRAMS

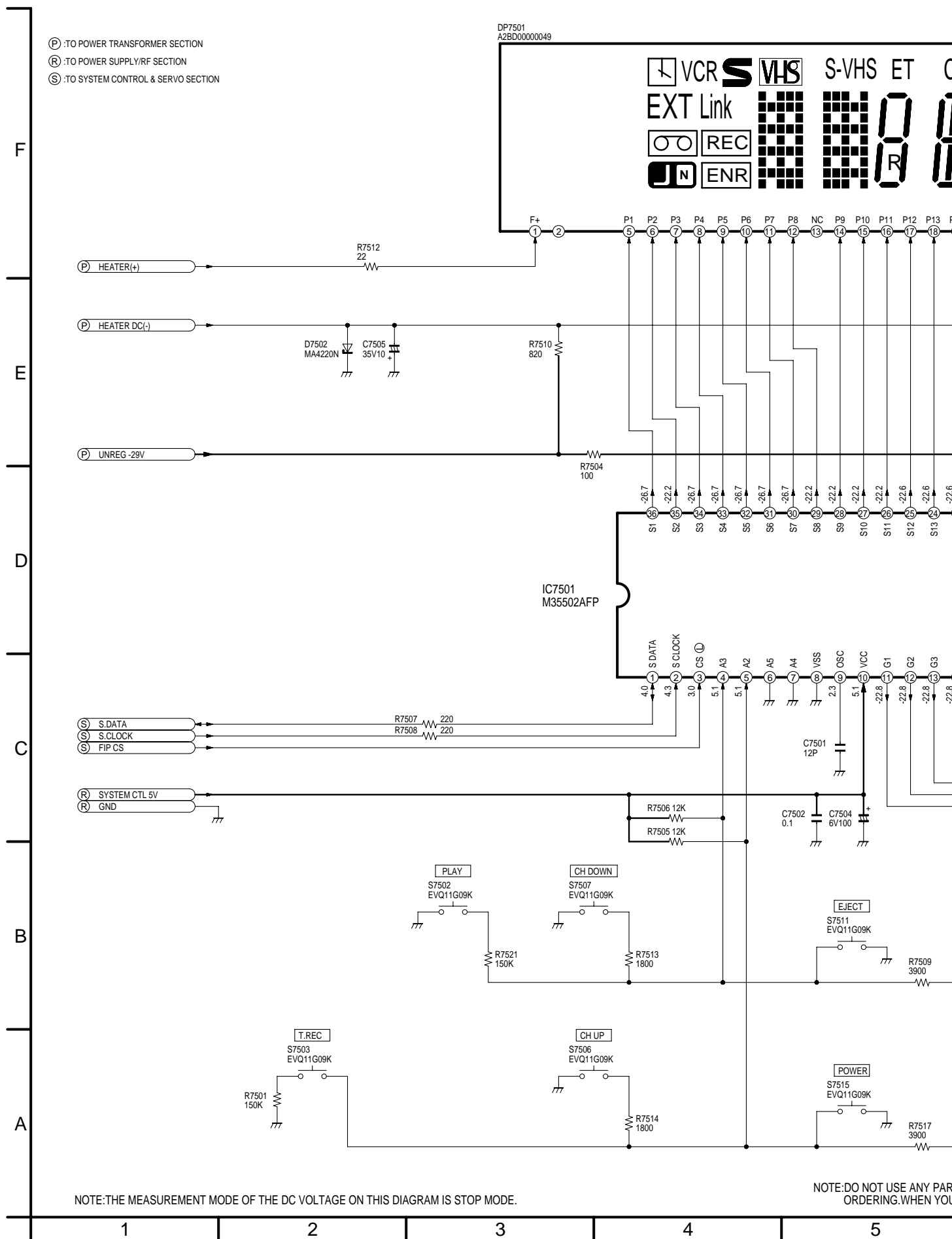
8.1. POWER TRANSFORMER SECTION IN MAIN SCHEMATIC DIAGRAM

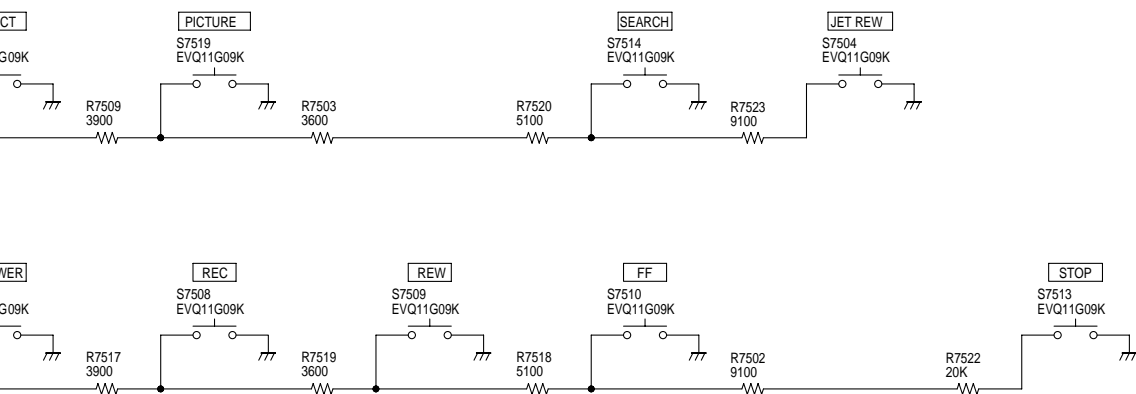
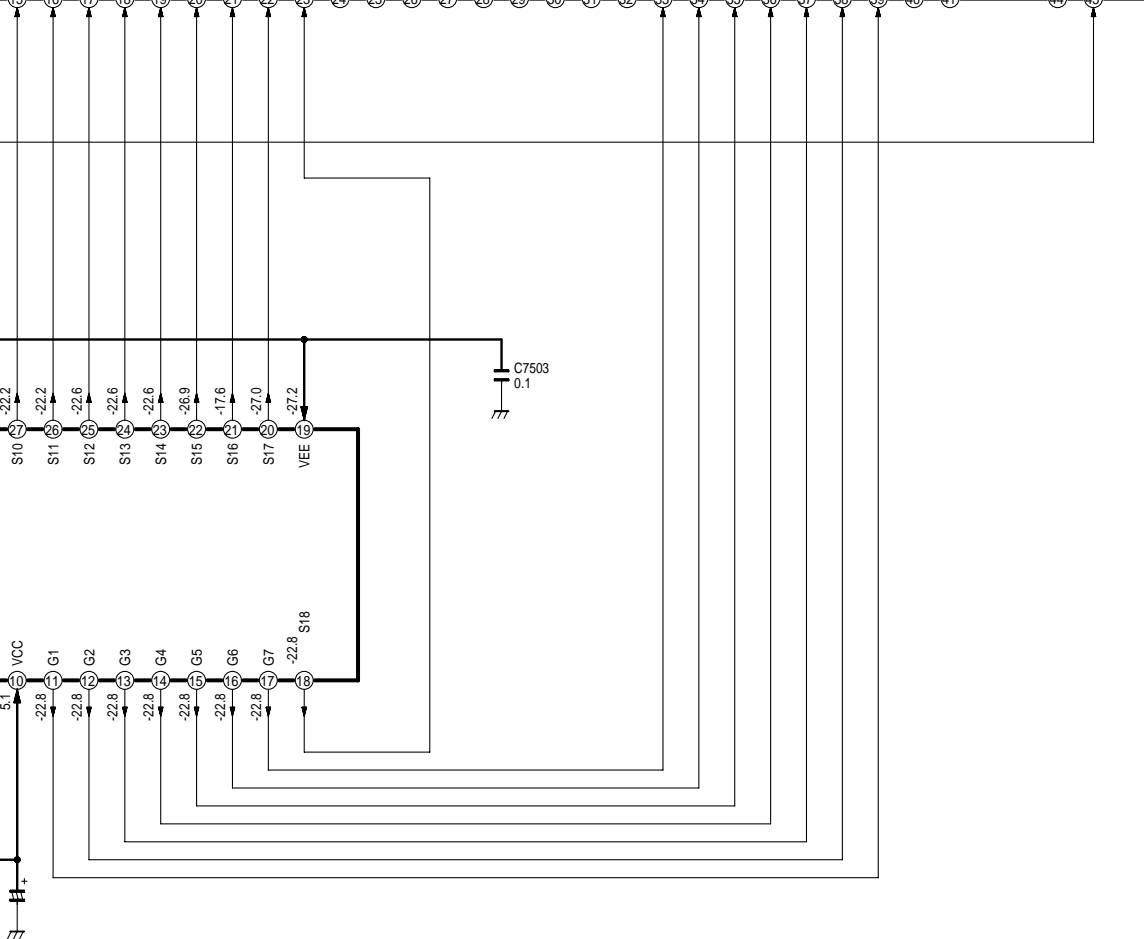
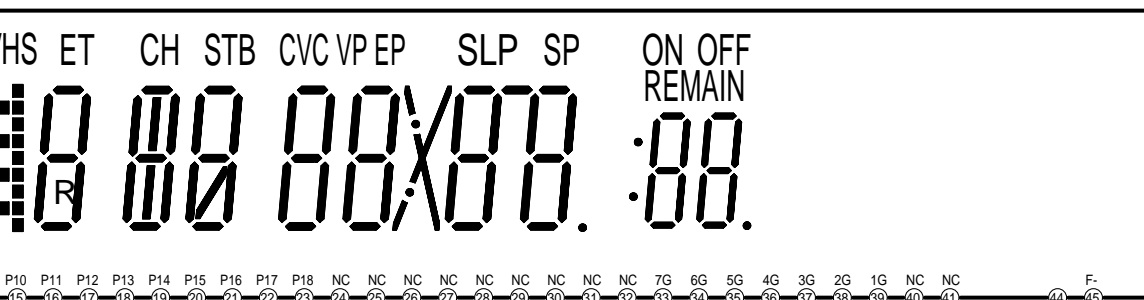


8.2. POWER SUPPLY/ RF SECTION IN MAIN SCHEMATIC DIAGRAM



8.3. TIMER SECTION IN MAIN SCHEMATIC DIAGRAM





DO NOT USE ANY PART NUMBER SHOWN ON THIS SCHEMATIC DIAGRAM FOR ORDERING.WHEN YOU ORDER A PART,PLEASE REFER TO PARTS LIST.

NV-SJ430PMP/ARU TIMER SECTION SCHEMATIC DIAGRAM

5

6

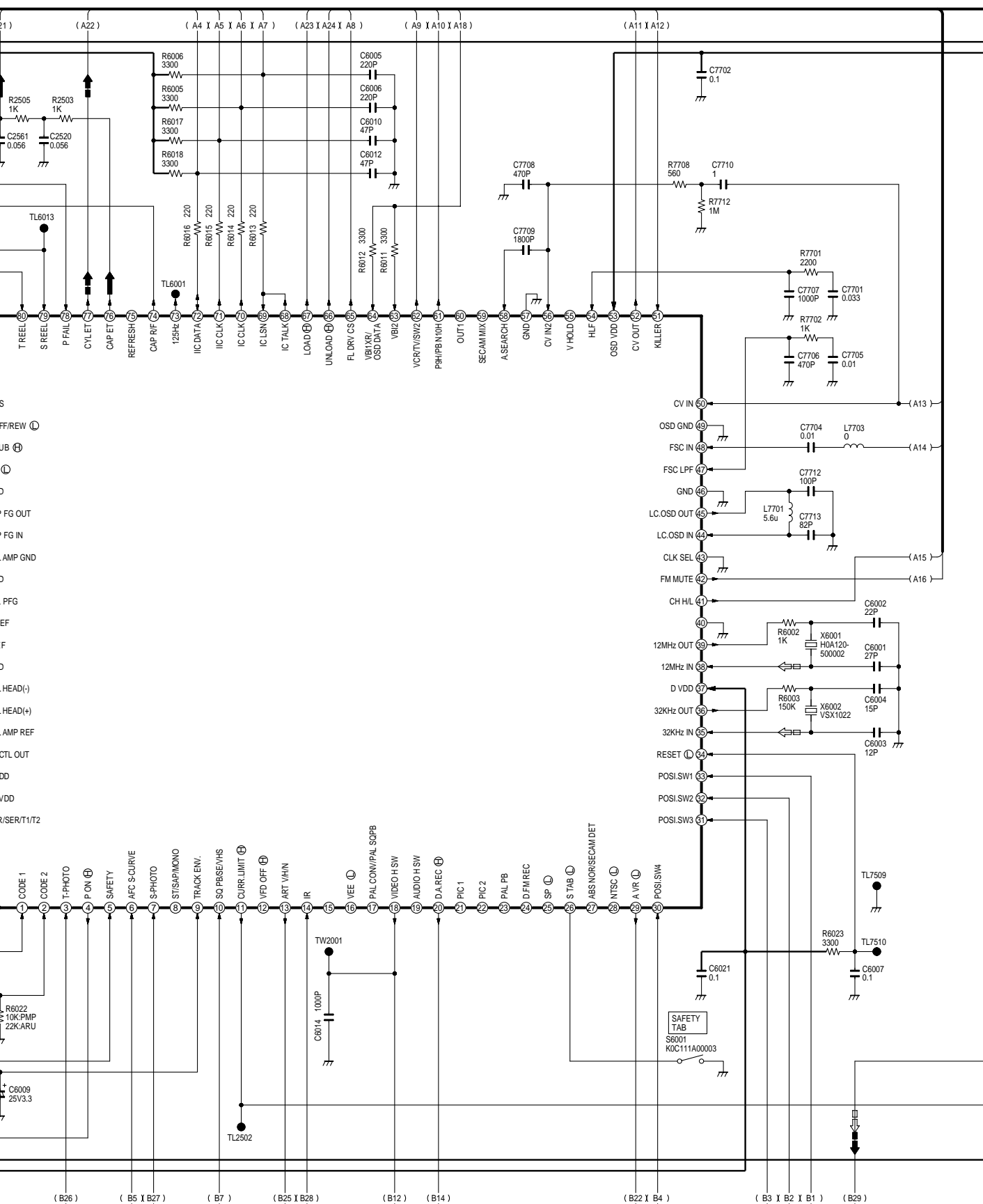
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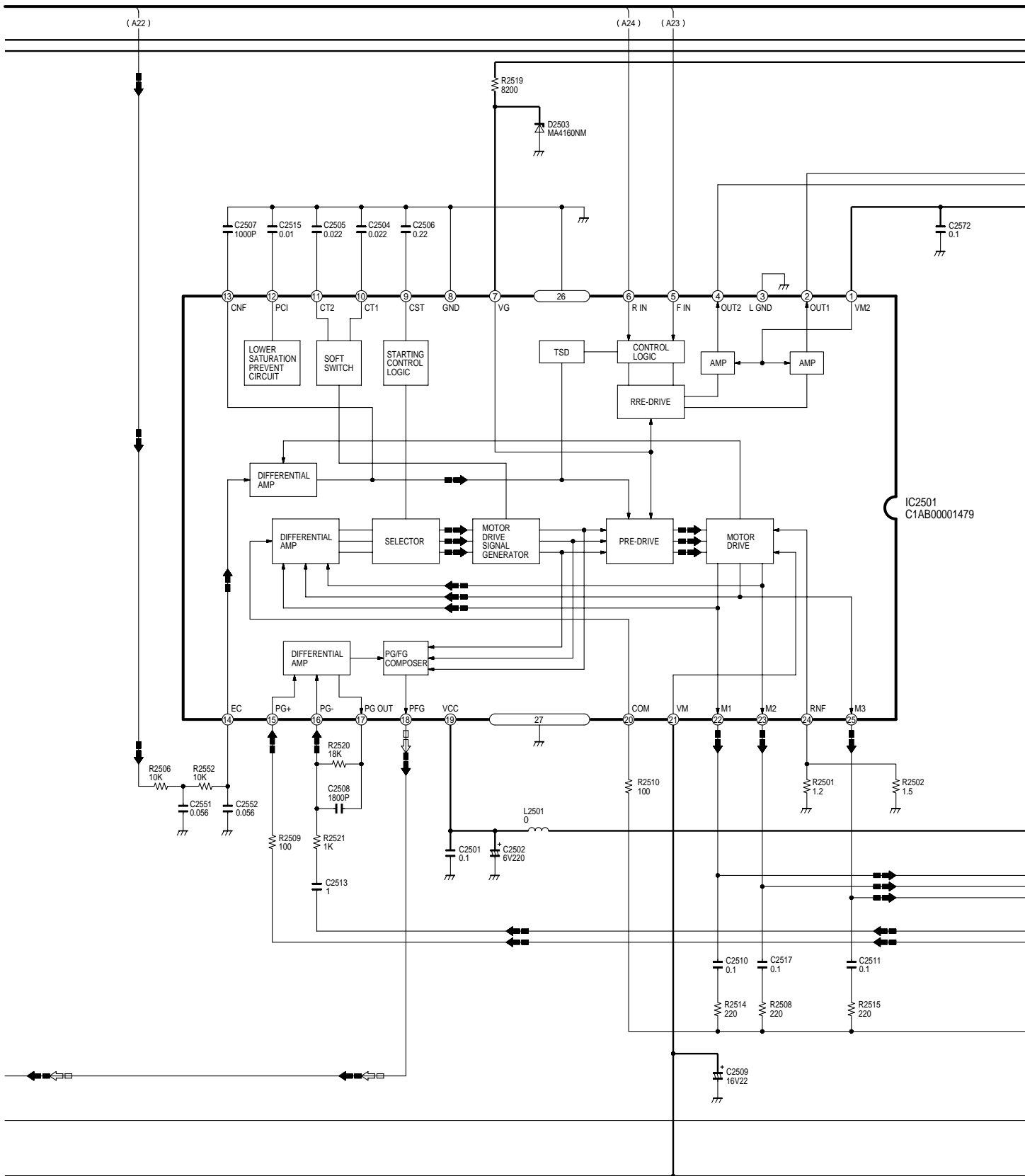


CYLINDER SERVO SPEED LOOP

CYLINDER SERVO PHASE LOOP



NV-SJ430PMP/ARU
SYSTEM CONTROL & SERVO SECTION SCHEMATIC DIAGRAM



NV-SJ430PMP/ARU
SYSTEM CONTROL & SERVO SECTION SCHEMATIC DIAGRAM

12

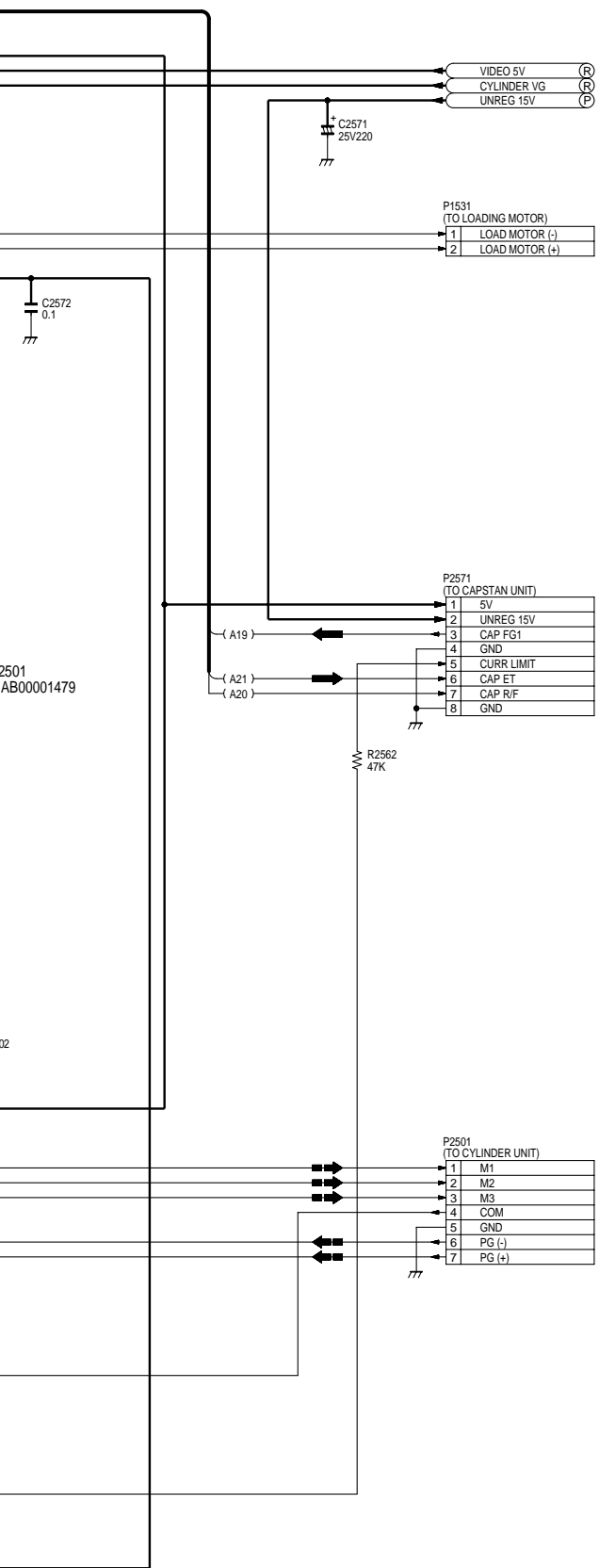
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14

15

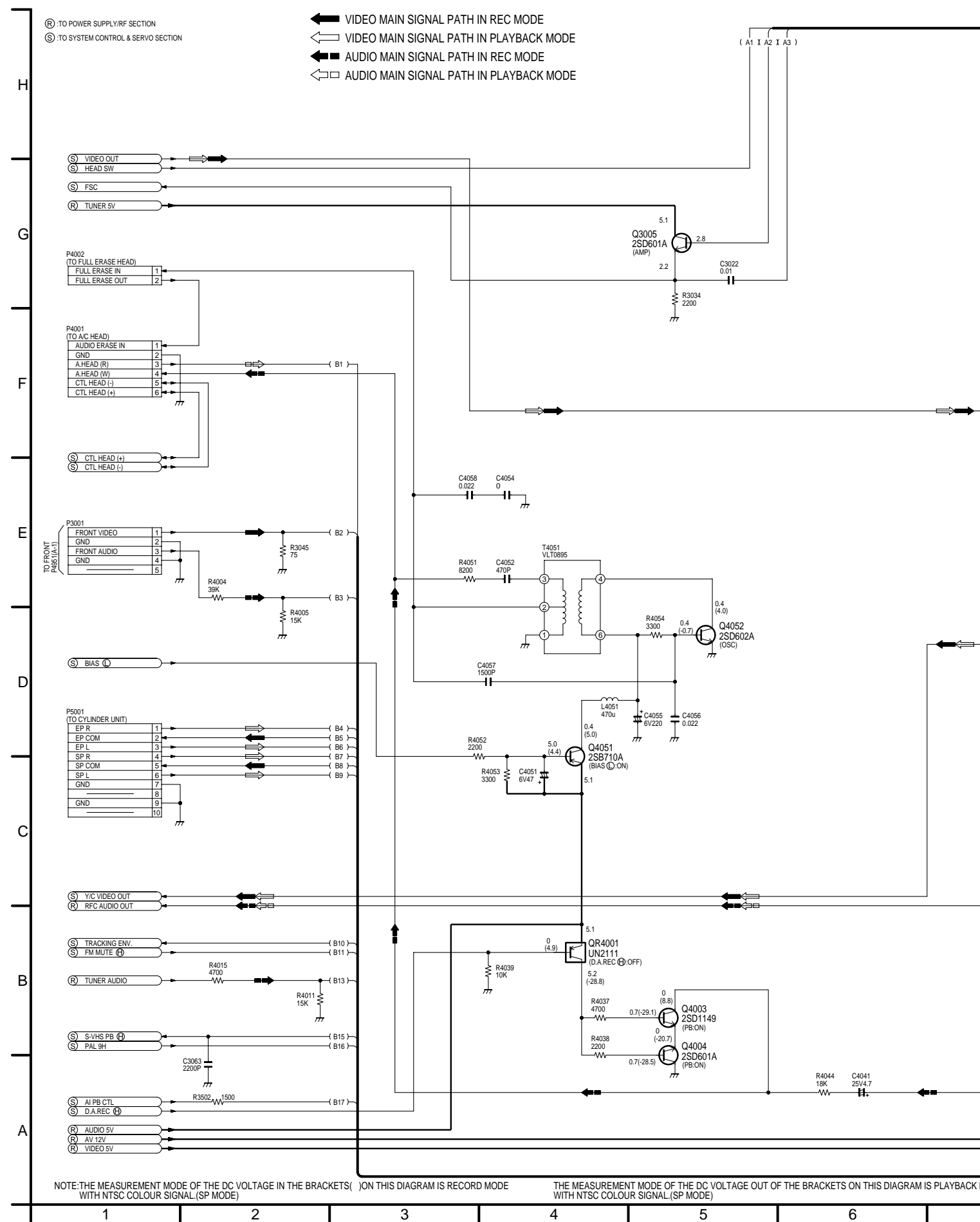
16

17



NV-SJ430PMP/ARU
SYSTEM CONTROL & SERVO
SECTION SCHEMATIC DIAGRAM

8.5. LUMINANCE & CHROMINANCE/ AUDIO SECTION IN MAIN SCHEMATIC DIAGRAM



C DIAGRAM

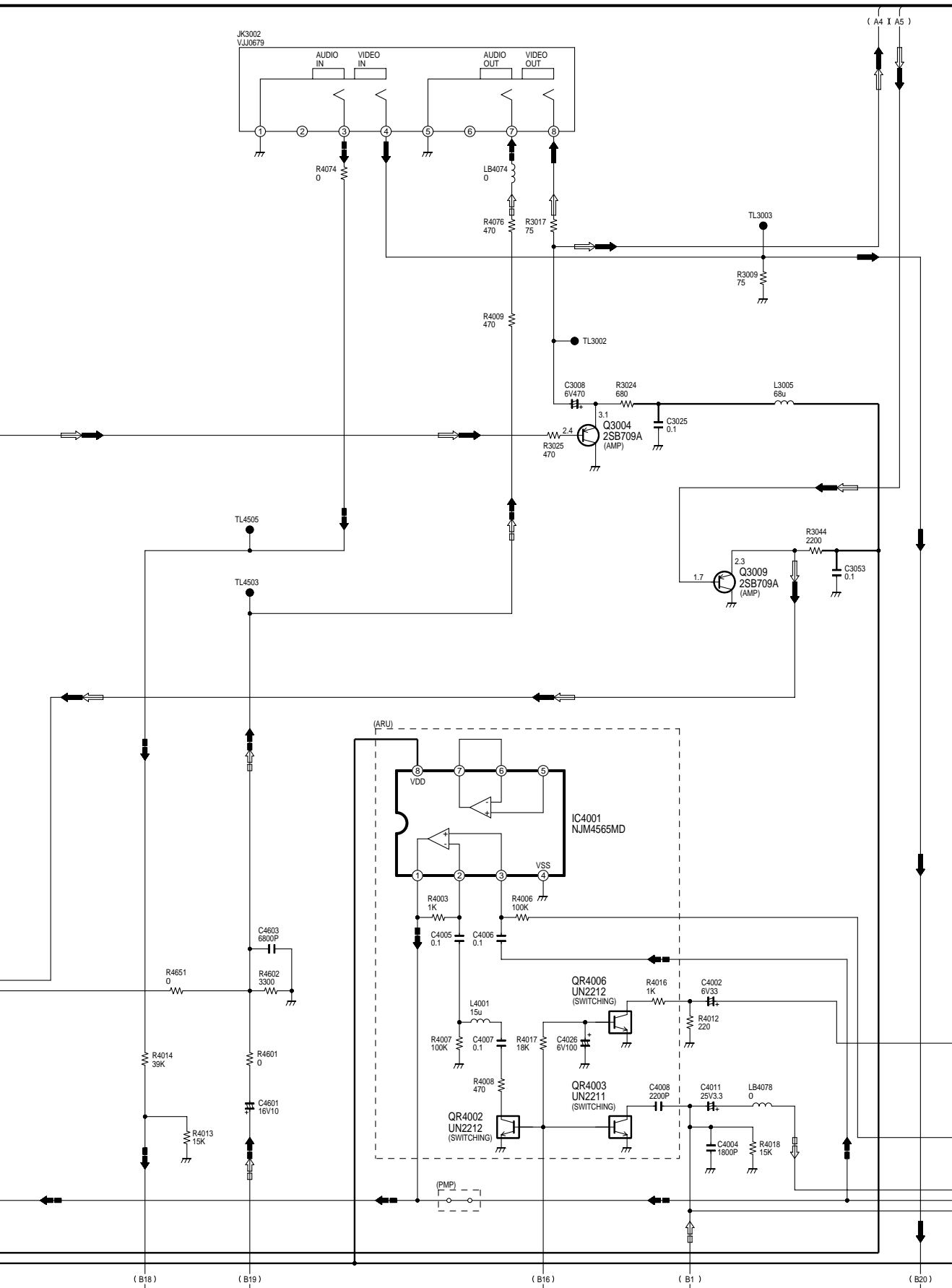


DIAGRAM IS PLAYBACK MODE

NV-SJ430PMP/ARU
LUMINANCE & CHROMINANCE/AUDIO SECTION SCHEMATIC DIAGRAM

7

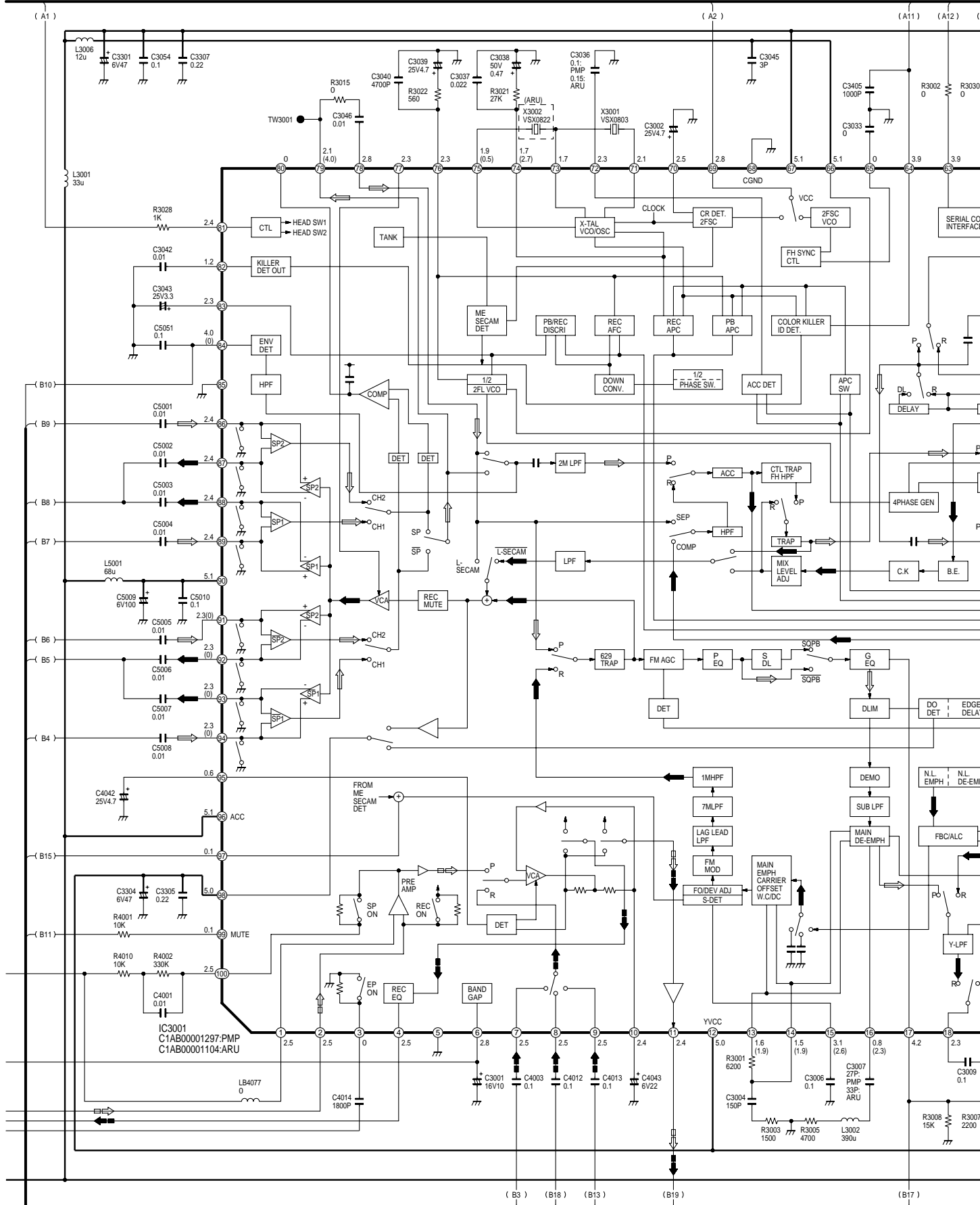
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NV-SJ430PMP/ARU
LUMINANCE & CHROMINANCE/AUDIO SECTION SCHEMATIC DIAGRAM

12

13

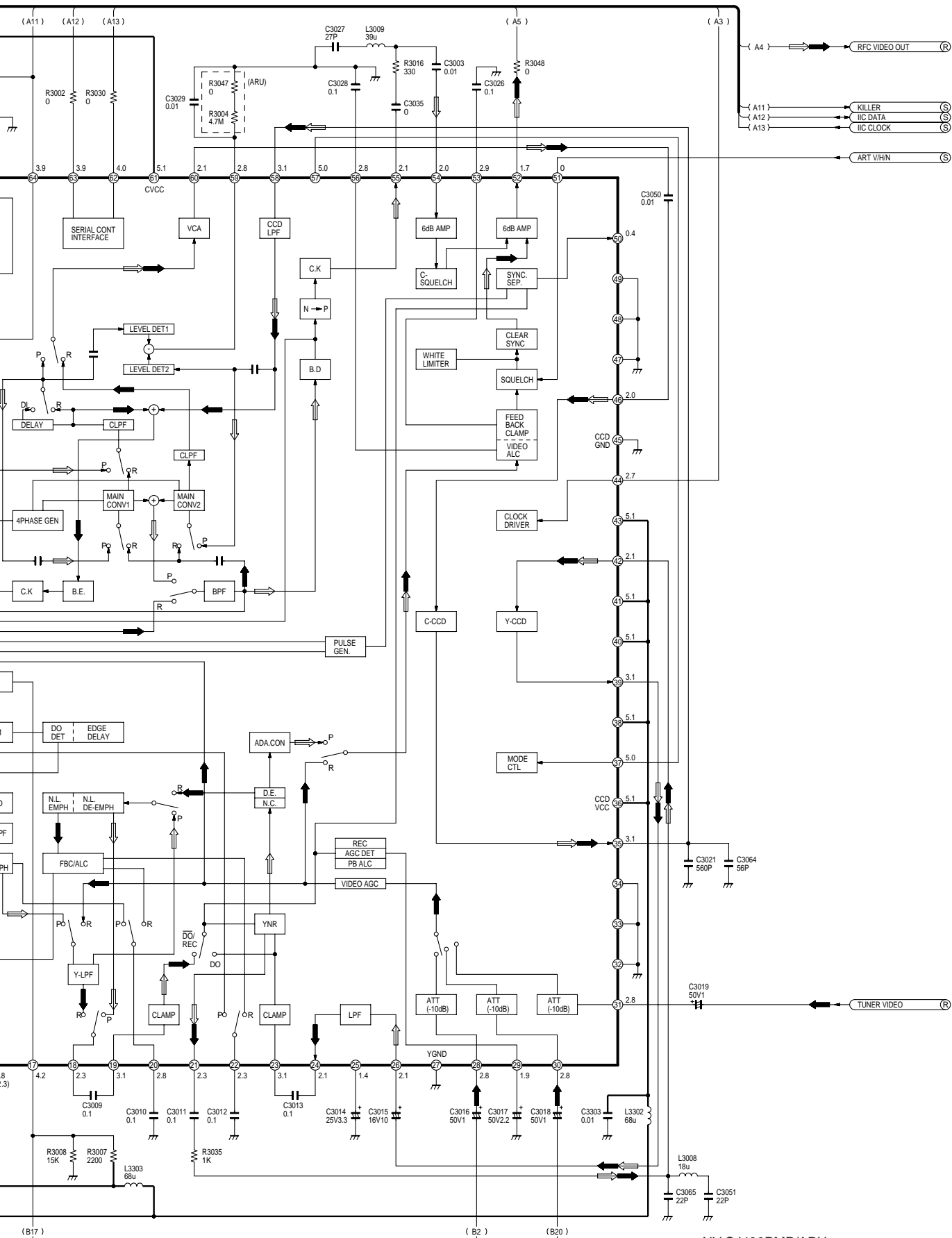
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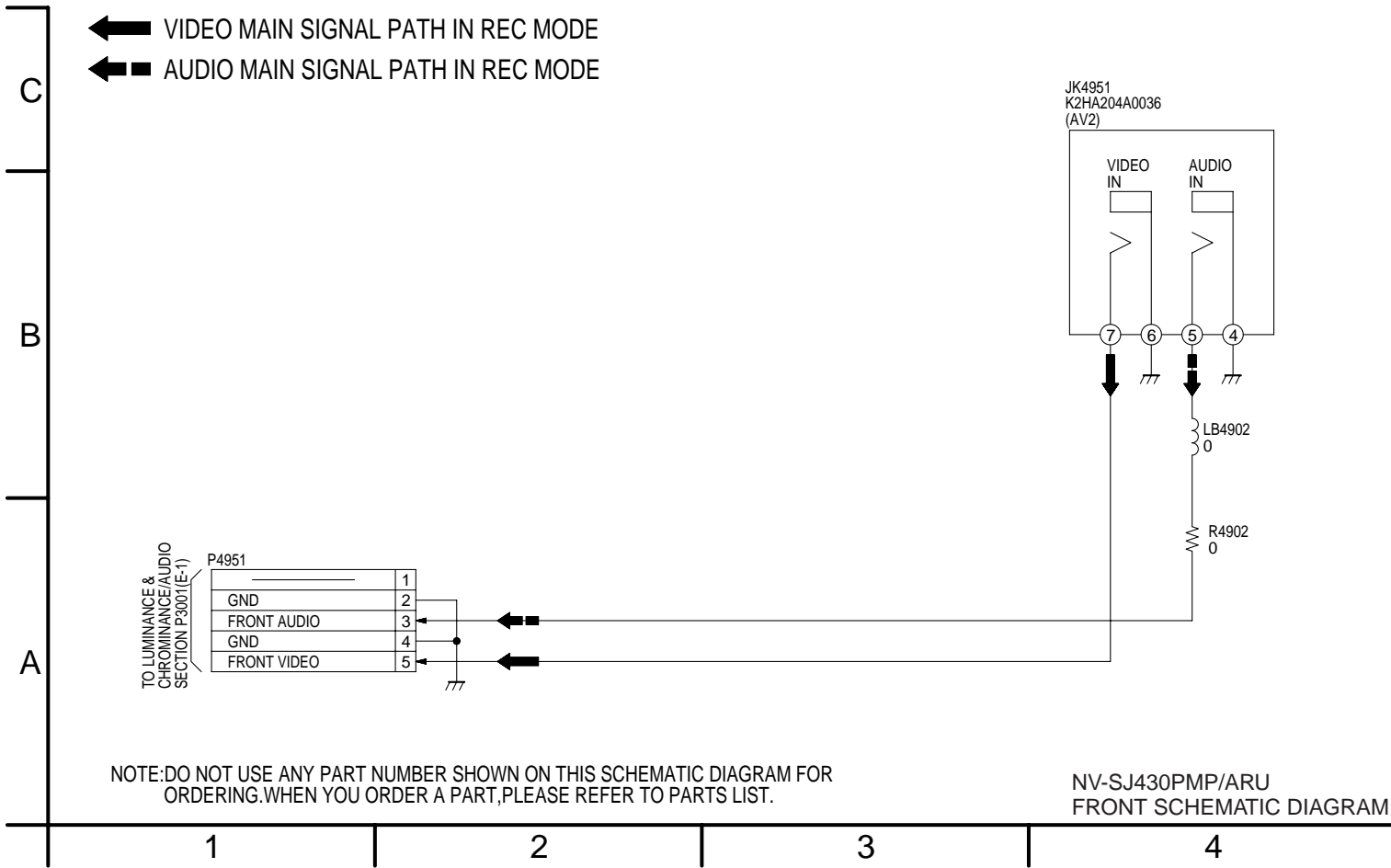
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8.6. FRONT SCHEMATIC DIAGRAM





CU
TIC DIAGRAM

